

UNITED STATES PATENT AND TRADEMARK OFFICE



UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/528,001	03/17/2000	Shiri Kadambi	P108339-00003	P108339-00003 3385	
32294 75	590 03/20/2006		EXAMINER		
SQUIRE, SANDERS & DEMPSEY L.L.P.			HOANG, THAI D		
	14TH FLOOR 8000 TOWERS CRESCENT		ART UNIT	PAPER NUMBER	
TYSONS CORNER, VA 22182			2668		
			DATE MAILED: 03/20/2006	DATE MAILED: 03/20/2006	

Please find below and/or attached an Office communication concerning this application or proceeding.

Advisory Action Before the Filing of an Appeal Brief

Application No.	Applicant(s)		
09/528,001	KADAMBI ET AL.		
Examiner	Art Unit		
Thai D. Hoang	2668		

- and a mind a mind of an an appear of a	Examiner	Art Unit				
	Thai D. Hoang	2668				
The MAILING DATE of this communication appe	ars on the cover sheet with the c	orrespondence add	ress			
THE REPLY FILED 22 February 2006 FAILS TO PLACE THIS	APPLICATION IN CONDITION FO	R ALLOWANCE.				
 The reply was filed after a final rejection, but prior to or or this application, applicant must timely file one of the follow places the application in condition for allowance; (2) a No a Request for Continued Examination (RCE) in compliance 	the same day as filing a Notice of wing replies: (1) an amendment, aff tice of Appeal (with appeal fee) in o	Appeal. To avoid aba idavit, or other evider compliance with 37 C	nce, which FR 41.31; or (3)			
time periods: a) The period for reply expires <u>03</u> months from the mailing da						
b) The period for reply expires on: (1) the mailing date of this A no event, however, will the statutory period for reply expire I.	ater than SIX MONTHS from the mailing	g date of the final rejecti	on.			
Examiner Note: If box 1 is checked, check either box (a) or TWO MONTHS OF THE FINAL REJECTION. See MPEP 7 Extensions of time may be obtained under 37 CFR 1.136(a). The date	06.07(f).					
have been filed is the date for purposes of determining the period of ex under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the set forth in (b) above, if checked. Any reply received by the Office later may reduce any earned patent term adjustment. See 37 CFR 1.704(b)	tension and the corresponding amount shortened statutory period for reply origi r than three months after the mailing da	of the fee. The approprinally set in the final Offi	iate extension fee ce action; or (2) as			
NOTICE OF APPEAL 2. The Notice of Appeal was filed on A brief in comp filing the Notice of Appeal (37 CFR 41.37(a)), or any exte a Notice of Appeal has been filed, any reply must be filed	nsion thereof (37 CFR 41.37(e)), to	avoid dismissal of th	ns of the date of e appeal. Since			
<u>AMENDMENTS</u>	·	• •				
 The proposed amendment(s) filed after a final rejection, They raise new issues that would require further co 	but prior to the date of filing a brief, nsideration and/or search (see NO	will <u>not</u> be entered b TE below);	ecause			
(b) ☐ They raise the issue of new matter (see NOTE below); (c) ☐ They are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for						
appeal; and/or						
(d) They present additional claims without canceling a NOTE:		ected claims.				
4. The amendments are not in compliance with 37 CFR 1.1		mpliant Amendment	(PTOL-324).			
Applicant's reply has overcome the following rejection(s)	:	•	,			
 Newly proposed or amended claim(s) would be all non-allowable claim(s). 			-			
7. For purposes of appeal, the proposed amendment(s): a) how the new or amended claims would be rejected is pro The status of the claim(s) is (or will be) as follows: Claim(s) allowed: Claim(s) objected to:		ll be entered and an e	explanation of			
Claim(s) rejected: <u>1-7</u> .						
Claim(s) withdrawn from consideration: AFFIDAVIT OR OTHER EVIDENCE						
 The affidavit or other evidence filed after a final action, bubecause applicant failed to provide a showing of good an was not earlier presented. See 37 CFR 1.116(e). 	nt before or on the date of filing a No d sufficient reasons why the affidav	otice of Appeal will <u>no</u> it or other evidence is	ot be entered s necessary and			
9. The affidavit or other evidence filed after the date of filing entered because the affidavit or other evidence failed to showing a good and sufficient reasons why it is necessar	overcome all rejections under appea	al and/or appellant fai	ils to provide a			
 The affidavit or other evidence is entered. An explanation REQUEST FOR RECONSIDERATION/OTHER 	n of the status of the claims after e	ntry is below or attach	ned.			
 The request for reconsideration has been considered bu <u>See Continuation Sheet.</u> 	t does NOT place the application in	n condition for allowar	nce because:			
12. Note the attached Information Disclosure Statement(s).	(PTO/SB/08 or PTO-1449) Paper N	lo(s)				
13.	6					
	HNzmjon	HANH PRIMARY	NGUYEN 'EXAMINER			
	$V \cup V$					

Continuation of 11. does NOT place the application in condition for allowance because:

Page 7 of the remarks, first paragraph, Applicants argue Muller I (Patent Number 5,909,686) does not teach the first stacking port and the second stacking port are communicatively connected through the first and second internet port interface controllers because "there are no additional elements shown or discussed in the transmission path after the stacking port 225". Examiner respectfully disagrees. Applicants are direct to col. 4, lines 50-53, wherein Muller discloses "An optional cascading interface 225 may include one or more internal links (not shown) for interconnecting switching elements to create larger switches". Thus, Muller clearly shows that a first switch communicates with a second switch through the interfaces 225.

Also, page 7, Applicants argue the IPIC is described in the specification "as including tables 9.1, a network buffer pool (NBP) 92, and arbiter 93, and flow control logic 94. Muller I does not teach any sort of an IPIC having these components". Examiner believes that this argument is not relevant because it is directed to subject matter not found in the claims.

Page 8 of the remarks, Applicants argue, "the office action does not indicate what elements described in Muller correspond to the elements recited in the rejected claims." Examiner respectfully disagrees. The office action clearly shows what Muller disclosed in parallel with the limitation recited in the claim, which is put in the parenthesis.

Page 9, Applicants argue, "the Office Action does not indicate any particular citation to a column or paragraph of Muller I that supports a broad conclusion" as shown in the office action. Examiner respectfully disagrees. The office action clearly shows twice on pages 3 and 6. Also, Applicants argue, "there is no teaching or disclosure that the packet headers have a module ID field added to the header by the first network switch." Examiner believes all data packets transmit in a packet switching system must have a header, which includes an ID field. It is a fundamental concept to one of ordinary skill in the art.

Regarding claims 3-6, pages 11-13, Applicants argue there is no teaching or suggestion to combine references. In response to applicant's argument that there is no suggestion to combine the references, the examiner recognizes that obviousness can only be established by combining or modifying the teachings of the prior art to produce the claimed invention where there is some teaching, suggestion, or motivation to do so found either in the references themselves or in the knowledge generally available to one of ordinary skill in the art. See In re Fine, 837 F.2d 1071, 5 USPQ2d 1596 (Fed. Cir. 1988)and In re Jones, 958 F.2d 347, 21 USPQ2d 1941 (Fed. Cir. 1992). In this case, both Muller I and Muller II are the same system (see figure 1 of Muller II and figure 2 of Muller I), wherein Muller II describes and claims element 220 of the Muller I.

Page 14, Applicants argue, "the flow control logic is more than a buffer memory controller that simply controls access to a memory, the flow control logic operates to check bits in the packet header and control packet flow." Examiner believes that this argument is not relevant because the claim 4 did not recite "check bits" function.